

N35L-U High Power LED

Introduction

The N35L-U LED from SemiLEDs brings industry leading technology to the solid state lighting market with its high quality and performance. With a silicone lens, N35L-U LEDs from SemiLEDs feature very high brightness and efficacy, as well as excellent lifetime.

N35L-U LEDs also feature a special design to fit secondary optics for various lighting applications. The user can easily get uniform light with any secondary optics.

Table of Contents

Characteristics	1
Relative Spectral Power Distribution	3
Typical Spatial Radiation Pattern	3
Thermal Design	4
Typical Forward L-I Characteristics	5
Typical Forward I-V Characteristics	5
Mechanical Dimensions	6
Recommended Solder Pad Design	7
Packing Information	8
Recommended Soldering Profile	9

RoHS Compliant

Characteristics

Absolute Ratings

Parameter	Rating		
Parameter	UV Series		
DC Forward Current (mA)	700 mA		
LED Junction Temperature	125 ℃		
LED Operating Temperature	-40°℃~125°℃		
Storage Temperature	-40°C ~125°C		
Soldering Temperature	Max. 260°C / Max. 10sec. (JEDEC 020c)		
ESD Sensitivity	2,000 V HBM (JESD-22A-114-B)		
Preconditioning	Acc. to JEDEC Level 2		

Notes:

- 1. Never operate the LEDs in reverse bias.
- 2. Do not drive at rated current for more than 5 seconds without proper thermal management.
- 3. When the LEDs are illuminating, operating current should be decided after considering the packages maximum temperature.
- 4. Caution: These devices emit high intensity UV/NUV light. Necessary precautions must be taken during operation. Do not look directly into the light or look through the optical system when in operation.

 Protective eyewear should be worn at all times during operation.
- 5. Lens discoloration may occur with prolonged exposure to UV/NUV light. Lens material will need to be tested for UV/NUV light compatibility and durability.

General Characteristics at 350mA

Part number	Color	Dominant Wavelength			Temperature	Thermal
				2θ _{1/2}	Coefficient	Resistance
		Λ _d Peak Wavelength λp *			of	Junction to
		Peak wave	iength vb ·		Vf (mV/℃)	Pad
		Min	Max		$\Delta V_F/\Delta T_J$	(°C/W) RΘ _{J-L}
N35L-U	U50	390	400	125	-2~-4	4.4

Notes:

1. The peak/dominant wavelength is measured with an accuracy of ±1nm





Luminous Flux and Forward Voltage

		Performance at Test Current (350mA)				Performance at 700mA	
Part number	Color	Group	Minimum	VF		Typical	
			Radiometric Power (mW)	Min	Max	Radiometric Power (mW)	
		D2	240	3.0	4.0	410	
N35L-U	U50	D3	280	3.0	4.0	475	
	(390~400nm)	D4	320	3.0	4.0	545	
		D5	360	3.0	4.0	615	

Note:

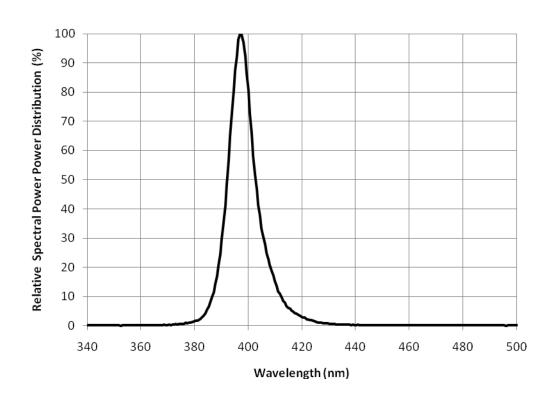
- 1. Radiometric power is measured with an accuracy of ±10%
- 2. The forward voltage is measured with an accuracy of $\pm 0.1V$

Forward Voltage Binning

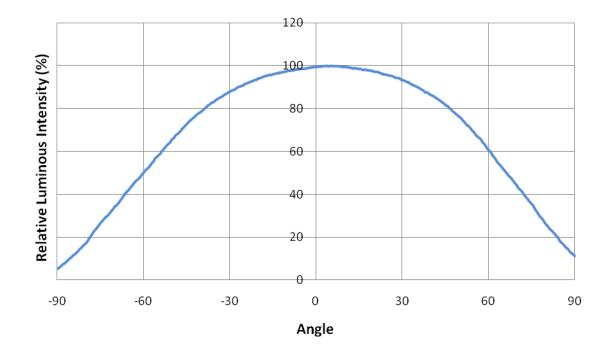
Part	Colon	Performance at Test Current (350mA)				
number	Color	Vf Group	Minimum (V)	Maximum (V)		
N35L-U	U50 (390~400nm)	V30	3.0	3.2		
		V32	3.2	3.4		
		V34	3.4	3.6		
		V36	3.6	3.8		
		V38	3.8	4.0		



Relative Spectral Power Distribution, Ta=25 °C



Typical Spatial Radiation Pattern

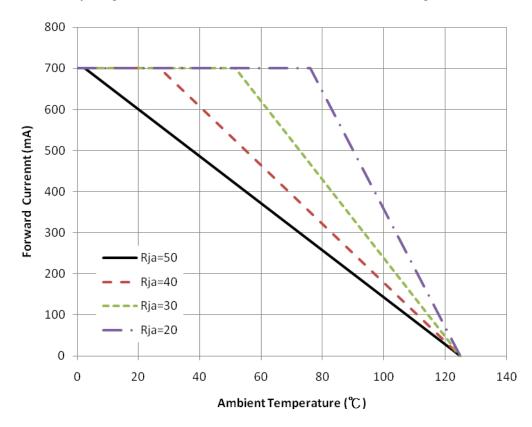






Thermal Design

Thermal design of the end product is important. The thermal resistance between the junction and the solder point $(R\Theta_{J-P})$ and the end product should be designed to minimize the thermal resistance from the solder point to ambient in order to optimize the emitter life and optical characteristics. The maximum operation current is determined by the plot of Allowable Forward Current vs. Ambient Temperature.



The junction temperature can be correlated to the thermal resistance between the junction and ambient (Rja) by the following equation.

$$Tj=Ta + Rja*W$$

Tj: LED junction temperature

Ta: Ambient temperature

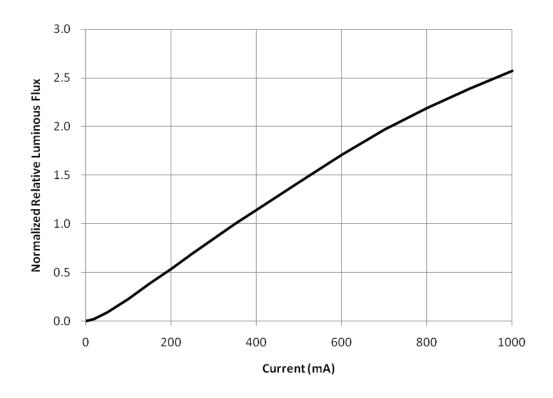
Rja: Thermal resistance between the junction and ambient

W: Input power (I_F*V_F)

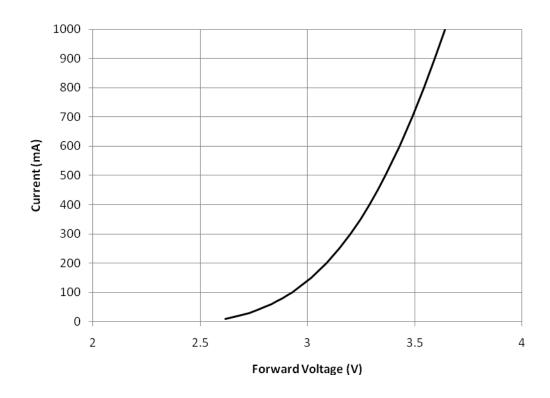




Typical Forward L-I Characteristics



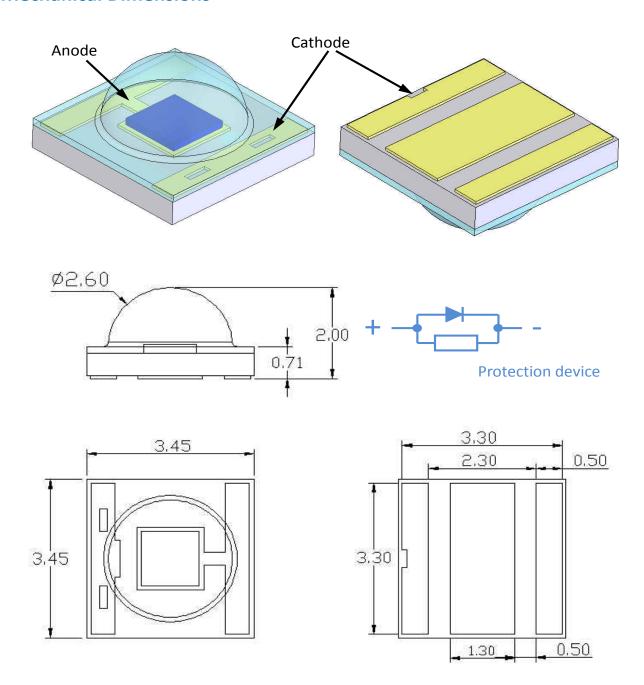
Typical Forward I-V Characteristics







Mechanical Dimensions



Notes:

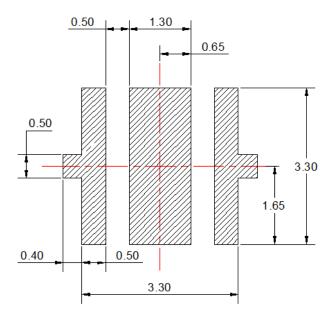
- 1. Drawing is not to scale
- 2. All dimensions are in millimeter
- 3. Dimensions are ± 0.13 mm unless otherwise indicated



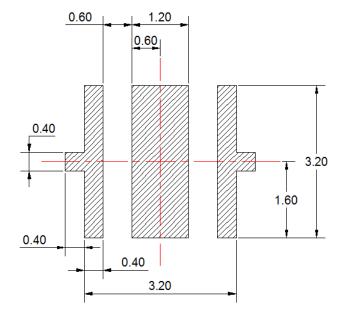


Recommended Solder Pad Design

Recommended Soldering Pad Design



Recommended Stencil Pattern Design (Mark Area is Opening)



Notes:

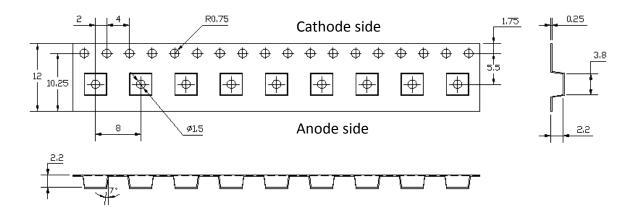
- 1. Drawing is not to scale
- 2. All dimensions are in millimeter

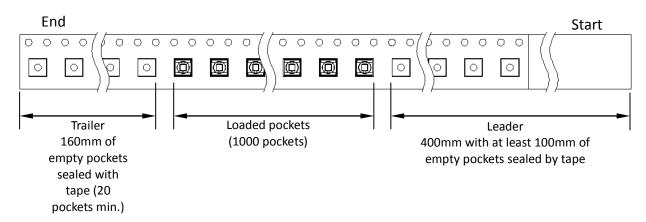


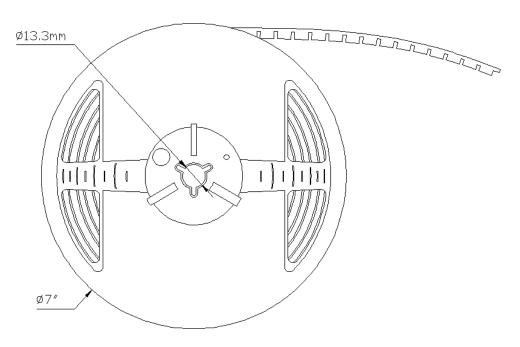


Packing Information

The carrier tape conform to EIA-481D.







Note:

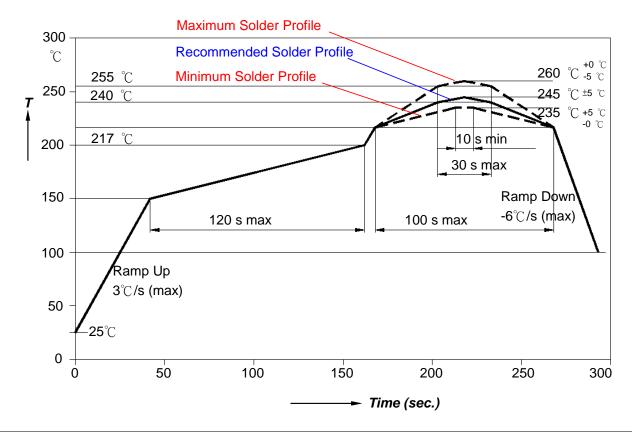
1. All dimensions are in millimeter.





Recommended Soldering Profile

The LEDs can be soldered using the parameter listed below. As a general guideline, the users are suggested to follow the recommended soldering profile provided by the manufacturer of the solder paste. Although the recommended soldering conditions are specified in the list, reflow soldering at the lowest possible temperature is advised for the LEDs.



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly	
Average Ramp-up Rate (Ts _{max} to Tp)	3°C/second max.	3℃/second max.	
Preheat			
 Temperature Min(Ts_{min}) 	100 ℃	150 ℃	
- Temperature Max(Ts _{max})	150 ℃	200 ℃	
- Time(ts _{min} to ts _{max})	60-120 seconds	60-180 seconds	
Time maintained above:			
 Temperature(T_L) 	183°C	217 ℃	
- Time(t _L)	60-150 seconds	60-150 seconds	
Peak/classification	215 ℃	260°C	
Temperature(Tp)			
Time within 5°C of actual Peak	10.20	20-40 seconds	
Temperature(tp)	10-30 seconds		
Ramp-Down Rate	6°C/second max.	6°C/second max.	
Time 25℃ to Peak Temperature	6 minutes max.	8 minutes max.	





Notice:

This product is *not for use, sale, or importation into the United States*. Any agreement, documentation, or communication (in any form, through any medium and for any purpose) related to this product is provided subject to the above disclaimer, and no (and seller hereby disclaims any) indemnification is provided for this product against any infringement claim of intellectual property rights in the United States. <u>The limitations above do not apply to products that are purchased and delivered before October 1, 2012.</u>





About Us

SemiLEDs Corporation is a US based manufacturer of ultra-high brightness LED chips with state of the art fabrication facilities in Hsinchu Science Park, Taiwan. SemiLEDs specializes in the development and manufacturing of vertical LED chips in blue (white), green, and UV using a patented copper alloy base. This unique design allows for higher performance and longer lumen maintenance. In December 2008, The World Economic Forum recognized SemiLEDs innovations with the 2009 Technology Pioneer Award. SemiLEDs is fully ISO 9001:2008 Certified

SemiLEDs is a publicly traded company on NASDAQ Global Select Market (stock symbol "LEDS"). For investor information, please contact us at investors@semileds.com.

For further company or product information, please visit us at www.semileds.com or please contact sales@ semileds.com.





www.semileds.com

ASIA PACIFIC

3F, No. 11, KeJung Rd. Chu-Nan Site Hsinchu Science Park Chu-Nan 350, Miao-Li County Taiwan, ROC

> Tel: +886-37-586788 Fax: +886-37-582688 sales@semileds.com



